



2006 International Workshop on Networking, Architecture, and Storages

August 1-3, 2006, Shenyang, China

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Up to 16 selected papers will be published in two special issues of the International Journal of High Performance Computing and Networking.

The last decade or so has seen rapid research and developments in computer technology, particularly the advent and proliferation of Internet and wireless networks, that has given way to a clear convergence of previously divert fields such as computer networking, architecture and storages. As a result, architectural designs are increasingly made more network-aware, if not network-centric; more classic architecture ideas are being adopted to network devices and protocols and vice versa; data grids and high-speed networks are making data ever more omnipresent and indeed the "life-blood" of computing and the main asset of any organization.

This workshop intends to bring together researchers and practitioners from academia and industry to discuss cutting edge research on networking (wired and wireless), high-performance computer architecture, and reliable data storage technologies. By discussing ongoing research, the workshop will expose participants to the most recent developments in these interdisciplinary areas. Topics of interest include but are not limited to:

- Ad hoc mobile networks
- Mobility models and systems
- Network applications and services
- Network architectures and management
- Optical networks
- Peer-to-peer communications
- Resource allocation and management
- Routing protocols,
- Security, trust, and privacy
- Self-organizing networks
- Sensor nets and embedded systems
- Virtual and overlay networks
- Web services and performance
- Wireless networks and protocols
- Processor architectures
- Cache and memory systems
- Parallel computer architectures
- Impact of technology on architecture
- Evaluation of storage architectures
- Parallel I/O architectures
- Storage management software
- Power-efficient architectures and techniques
- High-availability architectures
- High-performance I/O systems
- Embedded and reconfigurable architectures
- Interconnect and network interface architectures
- Innovative hardware/software trade-offs
- Impact of compilers on architecture
- Performance evaluation of real machines
- Storage Manageability, Reliability, Availability, and Security
- Storage Performance and Scalability
- File systems, Object-based storage, block-level storage
- Storage networking: e.g. Fibre Channel, InfiniBand, IP Storage, iSCSI

Important dates

- Paper submission: May 1 (Extended abstract up to 4 pages)
- Notification of acceptance: May 26
- Final Camera-ready paper: June 4

Online submission

<http://www.eece.maine.edu/iwnas/>

Keynote speakers (tentative)

- Dharma Agrawal, *U Cincinnati, USA*
- Laxmi Bhuyan, *UC Riverside, USA*
- Lionel Ni, *UST, Hong Kong*
- Zhiwei Xu, *Chinese Academy of Science, China*